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Inventor Name Search Result

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Last Name = ZERBE
First Name = JARED

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09776550	6873939	150	02/02/2001	METHOD AND APPARATUS FOR EVALUATING AND CALIBRATING A SIGNALING SYSTEM	ZERBE, JARED
09964911	7072415	150	09/27/2001	METHOD AND APPARATUS FOR GENERATING MULTI-LEVEL REFERENCE VOLTAGE IN SYSTEMS USING EQUALIZATION OR CROSSTALK CANCELLATION	ZERBE, JARED
09976170	7137048	150	10/12/2001	METHOD AND APPARATUS FOR EVALUATING AND OPTIMIZING A SIGNALING SYSTEM	ZERBE, JARED
10429514	7076377	150	05/05/2003	CIRCUIT, APPARATUS AND METHOD FOR CAPTURING A REPRESENTATION OF A WAVEFORM FROM A CLOCK-DATA RECOVERY (CDR) UNIT	ZERBE, JARED
10667355	Not Issued	93	09/23/2003	TECHNIQUE FOR UTILIZING SPARE BANDWIDTH RESULTING FROM THE USE OF A TRANSITION-LIMITING CODE IN A MULTI-LEVEL SIGNALING SYSTEM	ZERBE, JARED
10667492	Not Issued	30	09/23/2003	Technique for determining an optimal transition-limiting code for use in a multi-level signaling system	ZERBE, JARED
10672853	Not Issued	30	09/26/2003	Circuit, apparatus and method for adjusting a duty-cycle of a clock signal in response to incoming serial data	ZERBE, JARED
10849153	Not Issued	93	05/20/2004	TECHNIQUE FOR UTILIZING SPARE BANDWIDTH RESULTING FROM THE USE OF A TRANSITION-LIMITING CODE IN A MULTI-LEVEL SIGNALING SYSTEM	ZERBE, JARED
10878342	Not Issued	93	06/29/2004	TECHNIQUE FOR UTILIZING SPARE BANDWIDTH RESULTING FROM THE USE OF A CODE IN A MULTI-LEVEL SIGNALING SYSTEM	ZERBE, JARED
11422474	Not Issued	25	06/06/2006	METHOD AND APPARATUS FOR EVALUATING AND OPTIMIZING A	ZERBE, JARED

				SIGNALING SYSTEM	
11445702	Not Issued	40	06/02/2006	Circuit, apparatus and method for capturing a representation of a waveform from a clock-data recovery (CDR) unit	ZERBE, JARED
11455252	Not Issued	25	06/15/2006	Method and apparatus for generating multi-level reference voltage in systems using equalization or crosstalk cancellation	ZERBE, JARED
60450349	Not Issued	159	02/28/2003	Transition-limiting codes for 4-PAM signaling in high speed serial links	ZERBE, JARED
60494561	Not Issued	159	08/13/2003	Transition-limiting codes for 4-PAM signaling in high speed serial links	ZERBE, JARED
09572405	6340909	150	05/16/2000	Phase interpolator with noise immunity	ZERBE, JARED L.
09654643	Not Issued	41	09/05/2000	LOW-LATENCY EQUALIZATION IN MULTI-LEVEL, MULTI-LINE COMMUNICATION SYSTEMS	ZERBE, JARED L.
09910217	6516365	150	07/19/2001	APPARATUS AND METHOD FOR TOPOGRAPHY DEPENDENT SIGNALING	ZERBE, JARED L.
09953486	Not Issued	94	09/14/2001	MULTILEVEL SIGNAL INTERFACE TESTING WITH BINARY TEST APPARATUS BY EMULATION OF MULTILEVEL SIGNALS	ZERBE, JARED L.
09953514	Not Issued	168	09/14/2001	Built-in self-testing of multilevel signal interfaces	ZERBE, JARED L.
09993138	Not Issued	41	11/16/2001	Signal line routing to reduce crosstalk effects	ZERBE, JARED L.
10123370	6965262	150	04/15/2002	METHOD AND APPARATUS FOR RECEIVING HIGH SPEED SIGNALS WITH LOW LATENCY	ZERBE, JARED L.
10195128	Not Issued	61	07/12/2002	Signaling system having grouped reflection characteristics	ZERBE, JARED L.
10195129	Not Issued	41	07/12/2002	Selectable-tap equalizer	ZERBE, JARED L.
10195130	Not Issued	41	07/12/2002	Auto-configured equalizer	ZERBE, JARED L.
10195140	Not Issued	80	07/12/2002	Clock data recovery circuit with equalizer clock calibration	ZERBE, JARED L.
10359061	6684263	150	02/04/2003	APPARATUS AND METHOD FOR TOPOGRAPHY DEPENDENT SIGNALING	ZERBE, JARED L.
10431147	7005939	150	05/06/2003	INPUT/OUTPUT CIRCUIT WITH ON-CHIP INDUCTOR TO REDUCE PARASITIC CAPACITANCE	ZERBE, JARED L.
10662872	Not Issued	71	09/16/2003	Partial response receiver	ZERBE, JARED L.

10742247	7032057	150	12/19/2003	INTEGRATED CIRCUIT WITH TRANSMIT PHASE ADJUSTMENT	ZERBE, JARED L.
10763849	7024502	150	01/22/2004	APPARATUS AND METHOD FOR TOPOGRAPHY DEPENDENT SIGNALING	ZERBE, JARED L.
10805413	Not Issued	30	03/19/2004	Transparent multi-mode PAM interface	ZERBE, JARED L.
10849692	Not Issued	30	05/19/2004	Crosstalk minimization in serial link systems	ZERBE, JARED L.
10881433	7051129	150	06/29/2004	MEMORY DEVICE HAVING PROGRAMMABLE DRIVE STRENGTH SETTING	ZERBE, JARED L.
10882879	7051130	150	06/30/2004	INTEGRATED CIRCUIT DEVICE THAT STORES A VALUE REPRESENTATIVE OF A DRIVE STRENGTH SETTING	ZERBE, JARED L.
10966070	Not Issued	30	10/18/2004	Partial response receiver	ZERBE, JARED L.
11022468	Not Issued	30	12/22/2004	Multi-tone system with oversampled precoders	ZERBE, JARED L.
11022469	Not Issued	30	12/22/2004	Adjustable dual-band link	ZERBE, JARED L.
11073403	7032058	150	03/04/2005	APPARATUS AND METHOD FOR TOPOGRAPHY DEPENDENT SIGNALING	ZERBE, JARED L.
11181411	Not Issued	94	07/13/2005	INTEGRATED CIRCUIT DEVICE THAT STORES A VALUE REPRESENTATIVE OF AN EQUALIZATION CO-EFFICIENT SETTING	ZERBE, JARED L.
11193916	Not Issued	30	07/29/2005	RAM-DAC for transmit preemphasis	ZERBE, JARED L.
11274585	7126408	150	11/14/2005	METHOD AND APPARATUS FOR RECEIVING HIGH SPEED SIGNALS WITH LOW LATENCY	ZERBE, JARED L.
11336045	Not Issued	25	01/20/2006	High-speed signaling systems with adaptable pre-emphasis and equalization	ZERBE, JARED L.
11368012	Not Issued	30	03/03/2006	Low latency multi-level communication interface	ZERBE, JARED L.
11404502	Not Issued	41	04/14/2006	Partial response receiver with clock data recovery	ZERBE, JARED L.
11433409	Not Issued	20	05/12/2006	Built-in self-testing of multilevel signal interfaces	ZERBE, JARED L.
60230616	Not Issued	159	09/05/2000	2G/s/pin 4-PAM parallel bus interface with transmit crosstalk cancellation & equalization and integrating receivers	ZERBE, JARED L.
60445946	Not Issued	159	02/07/2003	Input/output circuit with on-chip inductor to reduce parasitic capacitance	ZERBE, JARED L.
60446467	Not	159	02/11/2003	Circuit, apparatus and method for	ZERBE, JARED L.

	Issued			capturing a representation of a waveform from a clock-data recovery (CDR) unit	
60446491	Not Issued	159	02/11/2003	Circuit, apparatus and method for adjusting a duty-cycle of a clock signal in response to incoming serial data	ZERBE, JARED L.
60461729	Not Issued	159	04/09/2003	Partial response receiver	ZERBE, JARED L.

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